

ABSTRACT

Systems and methods for producing a simulated off condition in a computing device. The computing device includes system components such as a power supply, processors and fans that are put into a low power state upon receiving a signal to power off the device. This provides the appearance to users that the computing device is off. The system components, however, remain enabled to run applications when the computing device is in the simulated off condition. If necessary, the device can be returned to a fully on condition to process applications that require the system components to be brought out of the low power state to execute.